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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/599,845	10/11/2006	Seiichi Yamamoto	19415-019US1 PCT-05R-207/	5460
26211 FISH & RICHA	7590 06/06/200 ARDSON P.C.	EXAMINER		
P.O. BOX 1022	•	MCCLOUD, RENATA D		
MIINNEAPOLI	S, MN 55440-1022		ART UNIT	PAPER NUMBER
			2837	
			MAIL DATE	DELIVERY MODE
			06/06/2008	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

		Applicat	Application No. Applicant(s)					
		10/599,	345	YAMAMOTO, SEIICHI				
Office Action Summary			er	Art Unit				
		RENATA	MCCLOUD	2837				
۔۔ Period for F	The MAILING DATE of this commur Reply	nication appears on ti	he cover sheet with	the correspondence a	ddress			
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication. - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication. - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).								
Status								
_	esponsive to communication(s) file	ed on 11 October 20	06					
·	Responsive to communication(s) filed on <u>11 October 2006</u> . This action is FINAL . 2b)⊠ This action is non-final.							
′=	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is							
•	closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.							
Disposition	of Claims		•					
	aim(s) <u>1-8</u> is/are pending in the a	onlication						
•	4a) Of the above claim(s) is/are withdrawn from consideration.							
	5) Claim(s) is/are allowed.							
·	6)⊠ Claim(s) <u>1-8</u> is/are rejected.							
·	·							
8) <u></u> CI	aim(s) are subject to restri	ction and/or election	requirement.					
Application	Papers							
9)∏ Th	e specification is objected to by th	e Examiner.						
<i>,</i> —	e drawing(s) filed on is/are		o) objected to by	the Examiner.				
Ap	oplicant may not request that any obje	ction to the drawing(s)	be held in abeyance	e. See 37 CFR 1.85(a).				
Re	eplacement drawing sheet(s) including	the correction is requ	ired if the drawing(s)	is objected to. See 37 C	FR 1.121(d).			
11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.								
Priority und	der 35 U.S.C. § 119							
 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of: 1. Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). 								
Attachment(s) 1) ⊠ Notice o 2) □ Notice o	e the attached detailed Office action	on for a list of the cer	tified copies not re 4) Interview Sun Paper No(s)/N	nmary (PTO-413)				
Paper No(s)/Mail Date <u>10/11/06</u> . 6) Other:								

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DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

2. Claims 1-8 rejected under 35 U.S.C. 102(e) as being anticipated by Ohmichi et al (US 6885225).

Claims 1,3,7: A motor drive circuit comprising: a PWM voltage generation circuit for generating a PWM voltage (69); and a PWM drive circuit for driving a motor based on the PWM voltage outputted from the PWM voltage generation circuit, wherein the PWM drive circuit includes a load driving field-effect transistor (11), a through rate control portion (82/83) for reducing a through rate of a voltage based on the PWM voltage and then feeding the resultant voltage to a gate of the load driving field-effect transistor, and a gate voltage control portion (103) for stopping an operation of the through rate control portion and pulling up or down a gate potential of the load driving field-effect transistor to a predetermined value upon detecting during a transition period of a gate voltage of the load driving field-effect transistor that an output voltage of the load driving field-effect transistor has almost been inverted and become approximately equal to a value obtained when the load driving field-effect transistor is completely on.

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Claims 2,4,8: as a result of detection of the PWM voltage and the output voltage of the load driving field-effect transistor, only when a value of the PWM voltage is found to be at a level at which the load driving field-effect transistor is turned on and the output voltage of the load driving field-effect transistor is found to be approximately equal to a value obtained when the load driving field-effect transistor is completely on, the gate voltage control portion stops the operation of the through rate control portion and pulls up or down the gate potential of the load driving field-effect transistor to the predetermined value (col. 27:34-53, 28:34-49).

Claim Rejections - 35 USC § 103

- 3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 4. Claims 5-6 rejected under 35 U.S.C. 103(a) as being unpatentable over Ohmichi et al in view of Yasohara et al (US 7362061).

Claims 5-6: Ohmichi et al teach the limitations of claims 3 and 4. Referring to claims 5-6, they do not teach the PWM voltage generation circuit generates the PWM voltage according to a rotor position of the motor. Yasohara et al teach a PWM generation circuit generates the PWM voltage according to a rotor position of the motor (col. 10:14-25). It would have been obvious to one having ordinary skill in the art at the time the invention was made to modify the apparatus of Ohmichi et al to generate the voltage as taught by Yasohara et al in order to drive the motor.

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Conclusion

Any inquiry concerning this communication or earlier communications from the examiner

should be directed to RENATA MCCLOUD whose telephone number is (571)272-2069. The

examiner can normally be reached on Mon.- Fri. from 5:30 am - 2pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's

supervisor, Lincoln Donovan can be reached on (571) 272-2800 ext. 37. The fax phone number

for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent

Application Information Retrieval (PAIR) system. Status information for published applications

may be obtained from either Private PAIR or Public PAIR. Status information for unpublished

applications is available through Private PAIR only. For more information about the PAIR

system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private

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would like assistance from a USPTO Customer Service Representative or access to the

automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/Renata McCloud/

Primary Examiner, Art Unit 2837

/R. M./

Primary Examiner, Art Unit 2837